

PATENT APPLICATION



Property Docket No.: 9898-204
 Applicant: Jae-Yoon SIM and Jei-Hwan YOO
 Filing Date: July 9, 2001

Serial No. 09/901,930

INFORMATION DISCLOSURE CITATION
FORM PTO-1449 (Modified)U.S. PATENT DOCUMENTS

<u>Exam</u>	<u>Ref</u>	<u>Document Number</u>	<u>Issue Date</u>	<u>Name</u>	<u>Class</u>	<u>Sub-Class</u>
<u>TDC</u>	<u>—</u>	6,097,665	August 1, 2000	Tomishima et al.	—	—
<u>TDC</u>	<u>—</u>	6,147,914	Nov. 14, 2000	Leung et al.	—	—

FOREIGN PATENT DOCUMENTS

<u>Document Number</u>	<u>Publication Date</u>	<u>Country</u>	<u>Name</u>
—	—	—	—
—	—	—	—
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OTHER DOCUMENTS

Exam Ref (Including Author, Title, Date, Pertinent Pages, etc.)
Init

TDC — 1998 Symposium on VLSI Circuits Digest of Technical Papers P94-95
 "A precise On-chip voltage Generator for a Giga-Scale DRAM with a Negative Word-line Scheme"

Examiner: T. Cunningham

Date Considered: 2/13/02

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